**HILDA – The Next Generation of High Performance Computing**



**George Yazigi**

**Digital Systems Architect and Manager**

**DARTeC Cranfield University**

**Monday, 11th October 2021**

**7.00 – 8.30pm**

**Online**

*(Registration starts at 6:45pm)*

**Register online at:**

**The next generation of High Performance Computing is not necessarily Quantum Computers, rather a mix between conventional high density GPU boosted nodes and containerised microservices integrated ecosystem hosted using a HyperConverged Infrastructure.**

Algorithms and or programs which used to take weeks and months to conclude are now concluding within hours or even minutes thanks to a robust delivery infrastructure and optimised architecture.

Let’s dive into HILDA, an Ecosystem built on the above-mentioned concepts.

**Mr. George Yazigi** is currently the Digital Systems Architect and Manager at the Digital Aviation Research Centre (DARTeC), Cranfield University.

He has more than 22 years of experience in software engineering, computer and data science, architecting and designing digital security systems and intelligent software platforms based on Big Data Analytics, Computational Intelligence and Machine Learning.

 **Agenda**

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| 6.45pm | Sign in – Welcome – Networking in the Green Room  |
| 7.00pm | HILDA – The Next Generation of High Performance Computing– George Yazigi |
| 7:45pm | Q & A and Networking |
| 8.00pm | BCS BEDS Branch 2021 AGM |
| 8.15pm | Q & A and Networking |
| 8.30pm | CLOSE |